

100

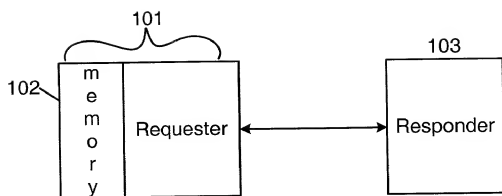


FIG. 1

```

sequenceDiagram
    participant M102 as Memory 102
    participant R101 as Requester 101
    participant R103 as Responder 103

    R101->>M102: Read Descriptor for message A
    M102->>R101: Data returned contains descriptor
    R101->>M102: Read Descriptor for message B
    M102->>R101: Data returned contains descriptor
    R101->>R103: Packet 1
    R101->>R103: Packet 2
    R101->>R103: Packet 3
    R101->>R103: Packet 4
    R103->>R101: Ack 1
    R103->>R101: Ack 2
    R101->>M102: Write Completion Code for message A
    R103->>M102: Ack 3
    R103->>M102: Ack 4
    R101->>M102: Write Completion Code for message B
  
```

The diagram illustrates the interaction between three components: Memory 102, Requester 101, and Responder 103. The sequence of events is as follows:

- Requester 101 sends a message to Memory 102: "Read Descriptor for message A".
- Memory 102 returns a message to Requester 101: "Data returned contains descriptor".
- Requester 101 sends a message to Memory 102: "Read Descriptor for message B".
- Memory 102 returns a message to Requester 101: "Data returned contains descriptor".
- Requester 101 sends four packets to Responder 103: "Packet 1", "Packet 2", "Packet 3", and "Packet 4".
- Responder 103 sends two acknowledgments back to Requester 101: "Ack 1" and "Ack 2".
- Requester 101 sends a message to Memory 102: "Write Completion Code for message A".
- Responder 103 sends two acknowledgments back to Memory 102: "Ack 3" and "Ack 4".
- Requester 101 sends a message to Memory 102: "Write Completion Code for message B".

FIG. 2
(PRIOR ART)

0000072.002704

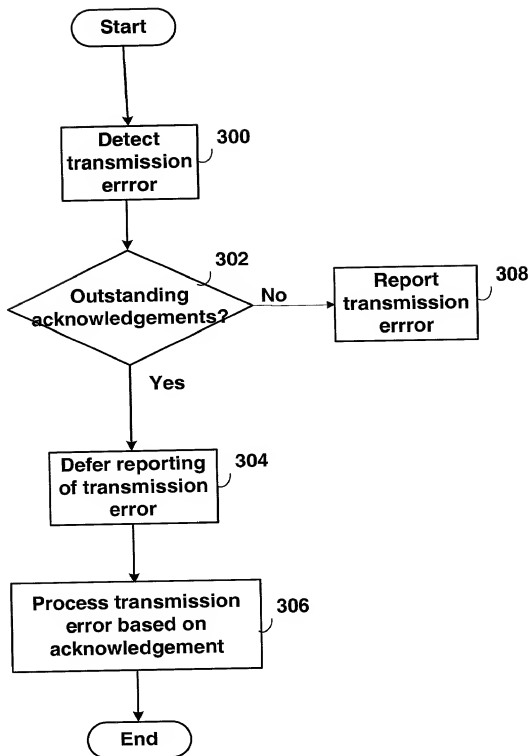


FIG. 3

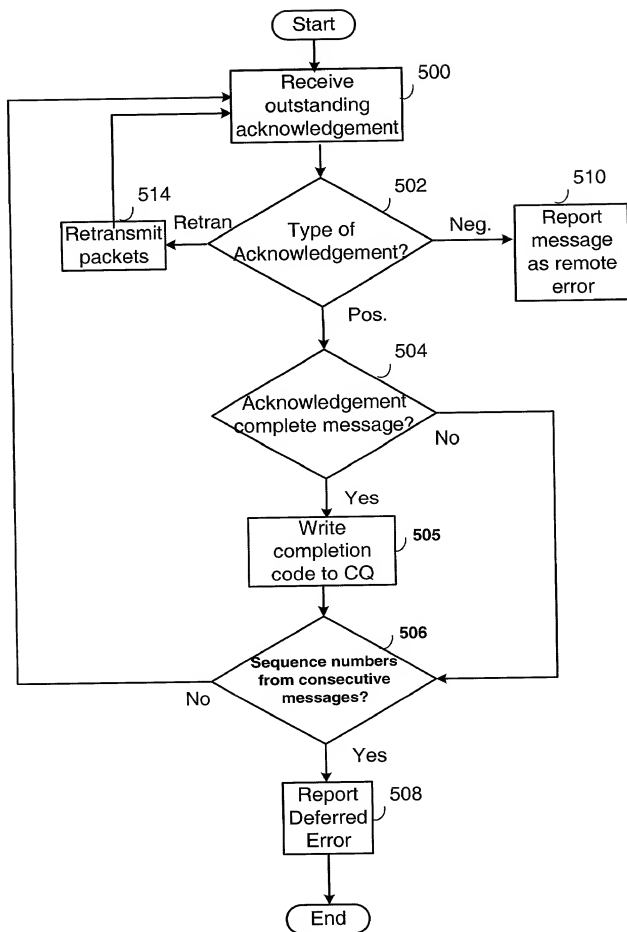


FIG. 5

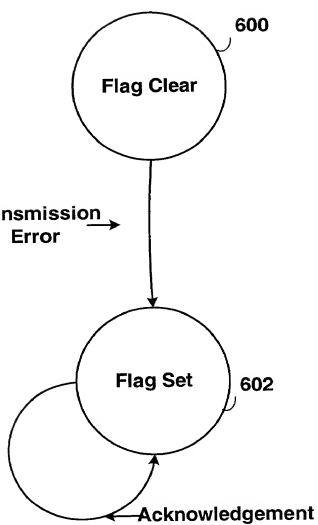


FIG. 6